

FIG. 5 (PRIOR ART)

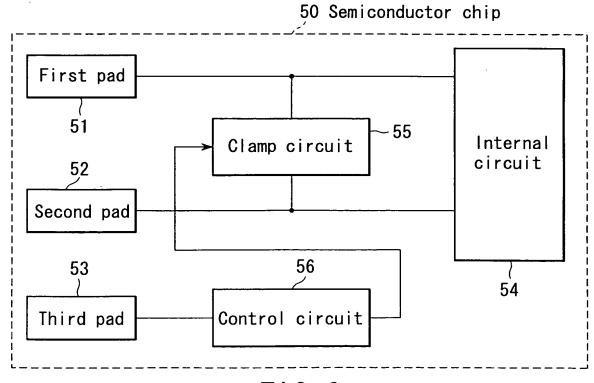
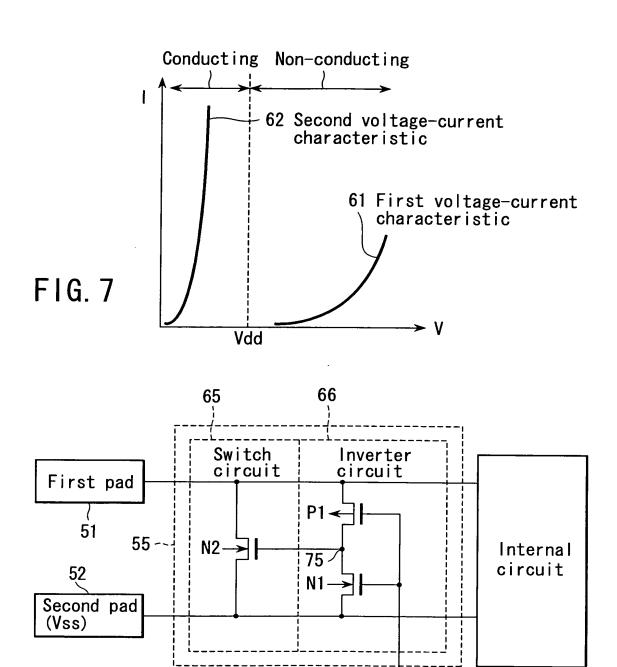


FIG. 6



~- 56

54

FIG. 8

53

Third pad

68

//~Vss

Resistor

